



ISSUE CLASSIFICATION

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U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/899436	D	710	104	2103 2111	K. S. G. WONG Aye

APPLICANTS
Harry Chue
Garry Tsang

System and method for efficiently implementing an electronic device architecture

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____				ISSUE FEE	
	(Assistant Examiner)	(Date)		Amount Due	Date Paid
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